IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit: Not Assigned

HOSONO, et al.

Examiner: Not Assigned

Serial No: Not Assigned

Filed: June 20, 2003

For: Non-Volatile Semiconductor Memory

Device and Electric Device with the Same

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

HOGAN & HARTSON, L.L.

B.

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Date: June 20, 2003

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)			Docket Number (Optional) 89165.0011		Application Number Not Assigned	
			Applicant HOSONO, et al.			
			Filing Date June 20, 20	Filing Date June 20, 2003		Group Art Unit Not Assigned
	U.	S. PATEN	T DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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	FORI	EIGN PATI	ENT DOCUMENTS		 	T == :
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION (YES/NO)
	2001-274364	10/05/01	JAPAN			Abstract
	OTHER DOCUMENTS (Including A	ıthor, Title, Date, Perti	nent Pages,	Etc.)	,
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EXAMINER: I	nitial if citation considered, whether or no	t citation is in	conformance with MPF	P § 609: Drav	w line through ci	itation if not in
conformance a	and not considered. Include copy of this	form with nex	t communication to the	applicant.		